

Patent
Docket No: 57181US002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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M. Brunson
1/9/03

In re Application of:

Paul F. Baude, Patrick R. Fleming, Michael A.
Haase, Tommie W. Kelley, Dawn V. Muires, and
Steven D. Theiss

Group Art Unit: 2814

Serial No.: 10/076,003

Filed: February 14, 2002

Examiner: H. Trinh

For: APERTURE MASKS FOR CIRCUIT FABRICATION

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January 6, 2003	Lucy C. Weiss

RESPONSE TO RESTRICTION REQUIREMENT

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Commissioner for Patents
Washington, DC 20231

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Dear Sir:

TECHNOLOGY CENTER 2800

This is in response to the Restriction Requirement mailed on December 4, 2002.

Remarks

Restriction to one of the following groups of claims was required under 35 U.S.C.

Section 121:

- Group I: Claims 1 – 8 and 38, said to be drawn to a wafer, classified in class 257, subclass 797, but actually termed a deposition system.
- Group II: Claims 9 – 19, drawn to a method, classified in class 438, subclass 761.
- Group III: Claims 20 – 33, drawn to an IC, classified in class 257, subclass 314.
- Group IV: Claims 34 – 37, drawn to a transistor, classified in class 257, subclass 347.

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Serial No.: 10/076,003
Our Reference: 57181US002

Date: January 6, 2003

Total number of pages: 3

Enclosed is a Response to Restriction Requirement for the following patent application:

Application of: Baude et al.

Serial Number: 10/076,003

Filed: February 14, 2002

Title: Aperture Masks for Circuit Fabrication

Please transmit the Response to the following Examiner:

Examiner: H. Trinh

Group Art Unit: 2814

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